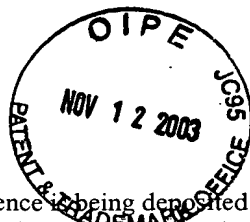


Docket No.: GR 98 P 5933



I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

By: 

Date: November 10, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/625,706 Confirmation No.: 8123  
Inventor : Thomas Ehben et al.  
Filed : July 23, 2003  
TC/A.U. : 2811  
Examiner : Cuong Nguyen

Docket No. : GR 98 P 5933

Customer No. : 24131

Hon. Commissioner for Patents  
Alexandria, VA 2231-1450

SUBMISSION OF REISSUE DECLARATION

Sir:

In response to the *Notice to File Corrected Application Papers*, dated September 10, 2003, enclosed please find an executed *Reissue Application Declaration by the Assignee* in accordance with 37 C.F.R. 1.63, accompanied by an executed *Statement under 37 C.F.R. 3.73(b)*.

Also enclosed is an abstract of the technical disclosure not exceeding 150 words and commencing on a separate sheet of paper, as set forth in 37 C.F.R. 1.72(b).

Respectfully submitted,

**LAURENCE A. GREENBERG**  
REG. NO. 29,308

  
For Applicants

Date: November 10, 2003  
Lerner and Greenberg, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: (954) 925-1100  
Fax: (954) 925-1101  
/bb

Integrated circuit having capacitive elements

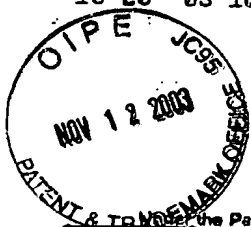
**Abstract**

An integrated circuit having capacitive elements for smoothing a supply voltage is described. In this case, at least one additional metal electrode, which is configured as a high frequency-optimized capacitance and is distinguished by an extremely low sheet resistance, is connected in parallel with the MOS capacitances. By connecting the areally highly effective MOS capacitance, which, however, is connected with a somewhat higher impedance, in parallel with areally less effective metal capacitances, which, however, are connected to the supply voltage in a very low-impedance manner, it is possible to obtain broadband buffering and thus decoupling of high-frequency interference signals. Very high-frequency interference components are attenuated on the chip and do not pass into the system surrounding the integrated circuit.

10-20-'03 16:46 FROM-Lerner &amp; Greenberg

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T-884 P02/06 U-596



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# REISSUE APPLICATION DECLARATION BY THE ASSIGNEE

Docket Number (optional)

GR 98 P 5933

I hereby declare that:

The residence, mailing address and citizenship of the inventor are stated below.

I am authorized to act on behalf of the following assignee: Infincon Technologies AG

and the title of my position with said assignee is: \_\_\_\_\_

The entire title to the patent identified below is vested in said assignee.

Inventor Thomas Ehben

Citizenship

German

Residence/Mailing Address Aidenbachstrasse 118, 81379 München, Germany

Inventor Thomas Steinecke

Citizenship

German

Residence/Mailing Address Am Hof 14, 85457 Hofsingelding, Germany

☒ Additional inventors are named on separately numbered sheets attached hereto.

Patent Number 6,645,868 B1 6,465,868 B1

Date of Patent Issued

October 15, 2002

Title of Invention Integrated Circuit Having Capacitive Elements

I believe said inventor(s) to be the original and first inventor(s) of the subject matter which is described and claimed in said patent, for which a reissue patent is sought on the invention entitled:

Integrated Circuit Having Capacitive Elements

the specification of which

☐ is attached hereto.

☒ was filed on July 23, 2003 as reissue application number 10,625,706

and was amended on \_\_\_\_\_

(If applicable)

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

☒ I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 385(b). Attached is form PTO/SB/2B (or equivalent) listing the foreign applications.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

☐ by reason of a defective specification or drawing.

☐ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☒ by reason of other errors.

This collection of information is required by 37 CFR 1.175. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE**REISSUE APPLICATION DECLARATION BY THE ASSIGNEE**

Docket Number (Optional)

GR 98 P 5933

At least one error upon which reissue is based is described as follows:

Applicants failed to submit the priority document prior to the issuance of the patent. Claim for priority under 35 U.S.C. 119 is herewith made. The claim is based on German patent application No. 198 58 114.9, filed December 16, 1998, as outlined in the attached PTO SB/2B. Every error in the patent which is being corrected in the present reissue application arose without any deceptive intention on the part of the applicant.

(Attach additional sheets, if needed.)

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant.

I hereby appoint:



Practitioners at Customer Number:

OR



Practitioner(s) named below:

Name	Registration Number
Laurence A. Greenberg	29,308
Werner H. Stemer	34,956
Ralph E. Locher	41,947
Gregory L. Mayback	40,719

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Correspondence Address: Direct all communications about the application to:



Customer Number:

OR



Firm or Individual Name

Lerner and Greenberg, P.A.

Address

P.O. Box 2480

Address

City

Hollywood

State

FL

Zip

33020

Country

Telephone

(954) 925-1100

Fax

(954) 925-1101

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

By: Dr. Willsau

For: Infineon Technologies AG

October 28, 2003

Date

By: Zedlitz

For: Infineon Technologies AG

October 28, 2003

Date

Address of Assignee: Ridlerstraße 55, 80339 München, Germany

10-20-'03 16:47 FROM-Lerncr &amp; Greenberg

+9549251101

T-884 P04/06 U-596

Patent No.: 6,465,868 B1

Docket Number: GR 98 P 5933

Application No.: 10/625,706

## Reissue Declaration

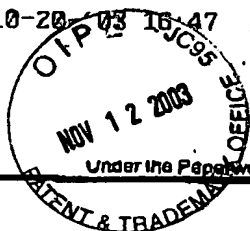
Sheet listing additional patentees

INVENTOR NAME		RESIDENCE		CITIZENSHIP
Given Name	Surname	City/Country	Postal Address	
Jens	Rosenbusch	München, Germany	Ludwig-Erhard-Allee 25 81739 München, Germany	German



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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Infineon Technologies AGApplication No./Patent No.: 6,465,868 B1 Filed/Issue Date: October 15, 2002Entitled: Integrated Circuit Having Capacitive ElementsInfineon Technologies AG

(Name of Assignee)

a

corporation

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of an undivided part interest

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel 011443, Frame 0884, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the Patent and Trademark Office at  
 Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
2. From: \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the Patent and Trademark Office at  
 Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
3. From: \_\_\_\_\_ To: \_\_\_\_\_  
 The document was recorded in the Patent and Trademark Office at  
 Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☐ Copies of assignments or other documents in the chain of title are attached.

(NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the PTO. See MPEP 302-302.8)

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

Dr. Willisau  
 By: \_\_\_\_\_  
 For: Infineon Technologies AG

October 28, 2003  
 Date

Zedlitz  
 By: \_\_\_\_\_  
 For: Infineon Technologies AG

October 28, 2003  
 Date